App. No. 10/652,627
Office Action Dated January 25, 2006

Amendments to the Claims:

This listing of claims will replace all prior versions and listing of claims in the application.

Claims 1 and 2 are amended.

Listing of Claims:

1. (Currently Amended) An image display system comprising:

a display memory for storing display image data to be displayed on a display panel;

an area image data generating section for supplying an area image data which is corresponding to a specific area of a captured image data by a image capturing device to said display memory, the supplying of said area image data generating section being provided by a DMA data transfer; and

a CPU which is coupled to said display memory and to said area image data generating section so as to perform controls thereof;

wherein said CPU stores a frame image data to be a frame image into said display memory before said area image data is transferred to said display memory, whereby said frame image data and said area image data are composed with each other to form an in-frame captured image data to be stored into said display memory, said in-frame captured image date being displayed on said display panel.

2. (Currently Amended) A display device comprising:

a display panel;

a display memory for storing a display image data to be displayed on a display panel;

storing means for storing a frame image data to be a frame image to be supplied to said display memory;

an image capturing device;

an area image data generating section for supplying an area image data which is corresponding to a specific area of an image data captured by said image capturing device to said display memory, the supplying of said area image data generating section being provided by a DMA data transfer; and

a CPU which is connected to said display panel, said display memory, said storing means, said image capturing device, and said area image data generating section so as to perform controls thereof,

wherein said CPU reads out a frame image data to be a frame image from said storing means and stores said frame image data into said display memory before said area image data is transferred to said display memory, whereby said frame image data and said area image data are composed with each other to form an in-frame captured image data to be stored into said display memory, said in-from captured image data being displayed on said display panel.

3. (Original) A display device according to claim 2, wherein said area image data generating section includes a buffer memory for storing said captured image data, specific area storing means for storing said specific area in said captured image, and a transfer address generating circuit for successively generating addresses of said specific area in said specific area storing means, wherein the addresses of said specific area generated by said transfer address generating circuit are also supplied to said buffer memory so that an image data specified by said addresses are successively read out and output.

4. (Original) A display device according to claim 2, wherein said area image data generating section supplies to said display memory with said area image data corresponding to said specific area as a valid data, while an image data other than said specific area is defined as an invalid data.

5. (Original) A display device according to claim 4, wherein said area image data generating section includes a buffer memory for storing said captured image data, specific area storing means for storing said specific area in said captured image, gate means for receiving a gate signal from said specific area storing means, and a read-out address generating circuit for generating a read-out address and supplying said read-out address to said buffer memory and

said gate means, wherein only said read-out address corresponding to said specific area stored in said specific area storing means is passed through said gate means so that said area image data is made valid, while the image date other than said area image data is made invalid.

6. (Previously Presented) A display device according to claim 3, wherein said specific area storing means includes an area memory for storing said specific area as an area map.

7. (Previously Presented) A display device according to claim 3, wherein said specific area storing means includes an area register used for determining said specific area in accordance with the coordinates for a plurality of points.

8. (Original) A display device according to claim 5, wherein said specific area storing means includes an area memory for storing said specific area as an area map, and said read-out address generating circuit includes an area register used for determining a specific area for generating a read-out address in accordance with the coordinates for a plurality of points.